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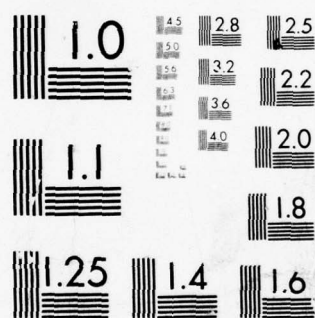
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MANUFACTURING METHODS AND TECHNOLOGY ENGINEERING
PROGRAM QUARTERLY TECHNICAL REPORT

A072504

Contract Number DAAB07-76-C-0040

INJECTION LASER DIODES FOR FIBER OPTIC COMMUNICATIONS

Prepared By:

LASER DIODE LABORATORIES, INC.
205 Forrest Street
Metuchen, New Jersey 08840

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Fifth Quarterly Report
for the Period 1 July 1977 to 30 September 1977

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Placed by:

U. S. Army Electronics Research and Development Command
Fort Monmouth, N. J. 07703

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MANUFACTURING METHODS AND TECHNOLOGY ENGINEERING
PROGRAM QUARTERLY TECHNICAL REPORT

Contract Number DAAB07-76-C-0040

INJECTION LASER DIODES FOR FIBER OPTIC COMMUNICATIONS

Prepared by:

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Applications Engineer

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Metuchen, New Jersey 08840



Fifth Quarterly Report
for the Period 1 July 1977 to 30 September 1977

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20. ABSTRACT (Continue on reverse side if necessary and identify by block number)		
→ The design and fabrication of injection laser diodes for use in fiber optic communications is discussed with regard to material synthesis, chip configuration, and device assembly in manufacturing environment. The opto-electronic source is based on the GaAs-GaAlAs double heterojunction structure and consists of a parallel array of lasers formed by the application of triple stripe geometry to the surface of the epitaxial wafer. The		

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SECTION I
INTRODUCTION

The primary objective of this Manufacturing Method and Technology Engineering Program is threefold. First, the Injection Laser Diode for use in Fiber Optic Communications as outlined in Specification SCS-516 must be transferred from a developmental device type to a volume manufactured commercial product without adversely affecting the performance characteristics of the device. Secondly, the manufacturing methods and techniques necessary for the volume production of the laser diode must be developed and implemented to insure the highest degree of device quality and reliability at a reasonable cost. Thirdly, verification of device performance and quality for injection lasers produced in a volume manufacturing environment must be carried out by means of rigorous testing and evaluation to demonstrate the technical adequacy of the manufacturing methods developed under this contract.

Major objectives for the fifth quarter of the program included the completion of pre burn-in testing of devices for the second engineering sample, burn-in of those devices, and initiation of the 2000 hour life test. Further work was done on all aspects of the test equipment, particularly in the construction of equipment for power, thermal impedance, and wavelength testing. Effort was also directed toward definition of specific test methods and procedures to be included in the test plan. While burn-in rack operation has continued without system failure, temporary air deflectors were found necessary to deal with excess heating of critical components.

SECTION II

MANUFACTURING METHODS AND TECHNOLOGY ENGINEERING

2.1 MATERIALS

Wafer production continues at a rate necessary to fulfill the anticipated needs for confirmatory sample devices. Wafer DHA-134 was evaluated as satisfactory and test results are reported under Device Testing.

The e-gun was removed from the vacuum system and sent to the manufacturer for repair and calibration. Upon its return, tests performed to check its operation revealed that although some of the problems were cleared up, the system would not be capable of the required performance. Therefore, an e-gun from a different manufacturer has been ordered, to be delivered in November.

2.2 TEST EQUIPMENT

The equipment for the program continues to see steady progress, minor setbacks notwithstanding. The burn-in rack has proven to be reliable, but excess heating of critical components has been observed. Temporary air deflectors have alleviated the problem somewhat, but further expansion (past 16 positions) must await a better solution to the problem. Although various methods of improved ventilation are under consideration, no further work is anticipated until completion of the 2000 hour life test which is currently in progress. The only other change made to the design of the burn-in rack during this quarter is the addition of card

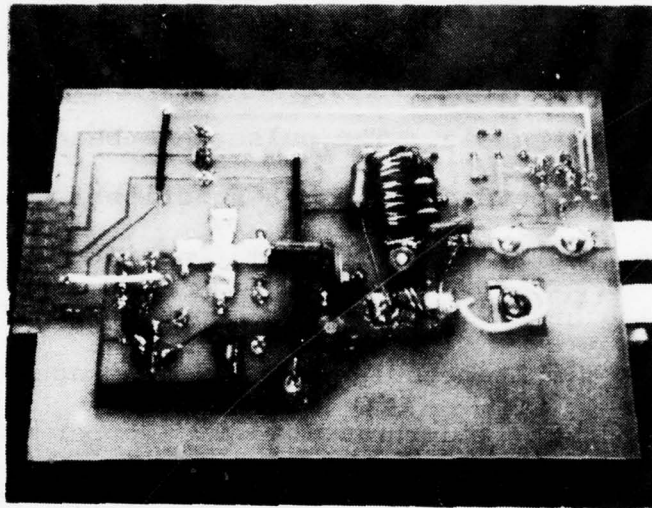
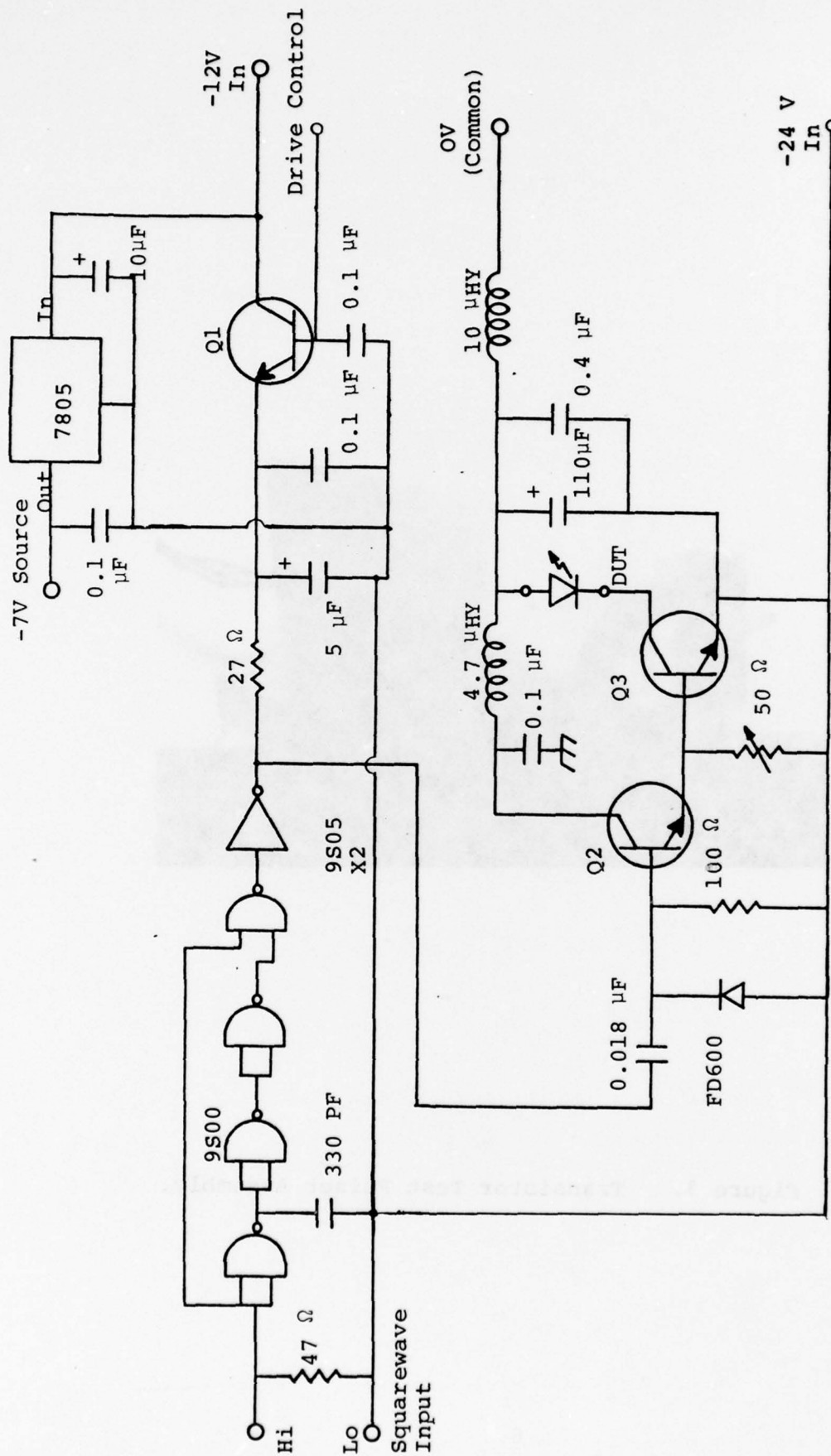


Figure 1. Burn-In Position Board with Edge Connector Fingers.

edge connectors. (See Figure 1) In addition to simplifying the wiring harness of each row of eight positions, card edge connectors reduce the problem of removal and exchange of individual positions from a major rewiring job to simply sliding the position boards in and out of their respective slots.

The transistor pulser design previously reported (Figure 2) has been tested extensively in use atop the goniometer (Figure 3) and its performance continues to be acceptable. The pulser has been integrated from its breadboard form into a functional housing incorporating heatsinking aluminum fins into the top of the housing and a large copper block at the front which serves simultaneously as a pill package holder, electrical contact, and thermal sink. Copper was chosen for its high thermal conductivity and high volume specific heat. The size of the block was chosen to allow no more than 1°C rise through a two minute test cycle. Its bottom section is radiused to permit 90 degree rotation thus allowing far field scans in any plane from perpendicular through parallel, as illustrated in Figure 4 . A substantially identical pulser for use in the thermally controlled test fixture has, however, encountered some problems. Minor differences in physical layout and grounding appear to have major effects on the stability of the system such that the amplitude of the laser current pulse exhibits discontinuous jumps as the nominal drive level is increased.



- Notes: 1. N.B. Unconventional Supply Arrangements
2. 9S05 Has All Gates in Parallel
3. Q1 = TIP29C
4. Q2 = CD1194
5. Q3 = CD1197

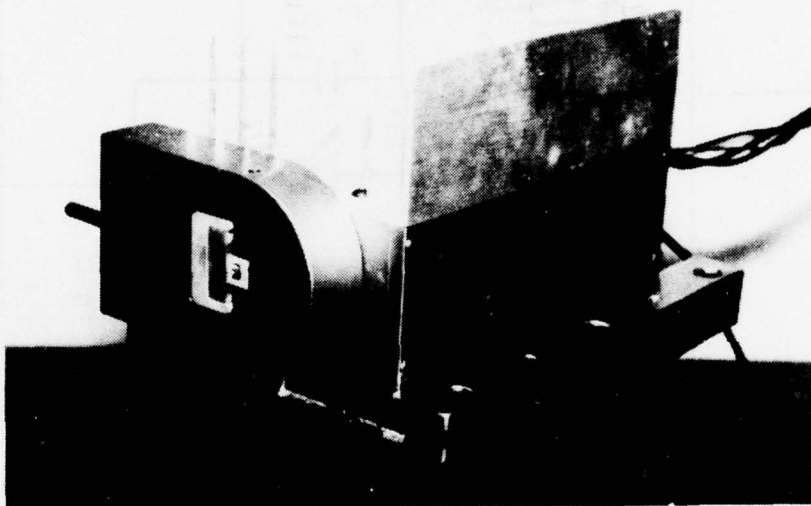


Figure 3. Transistor Test Pulser Assembly.

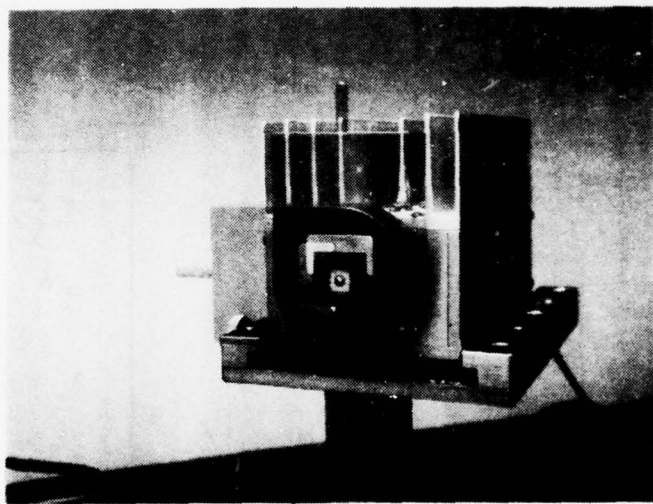
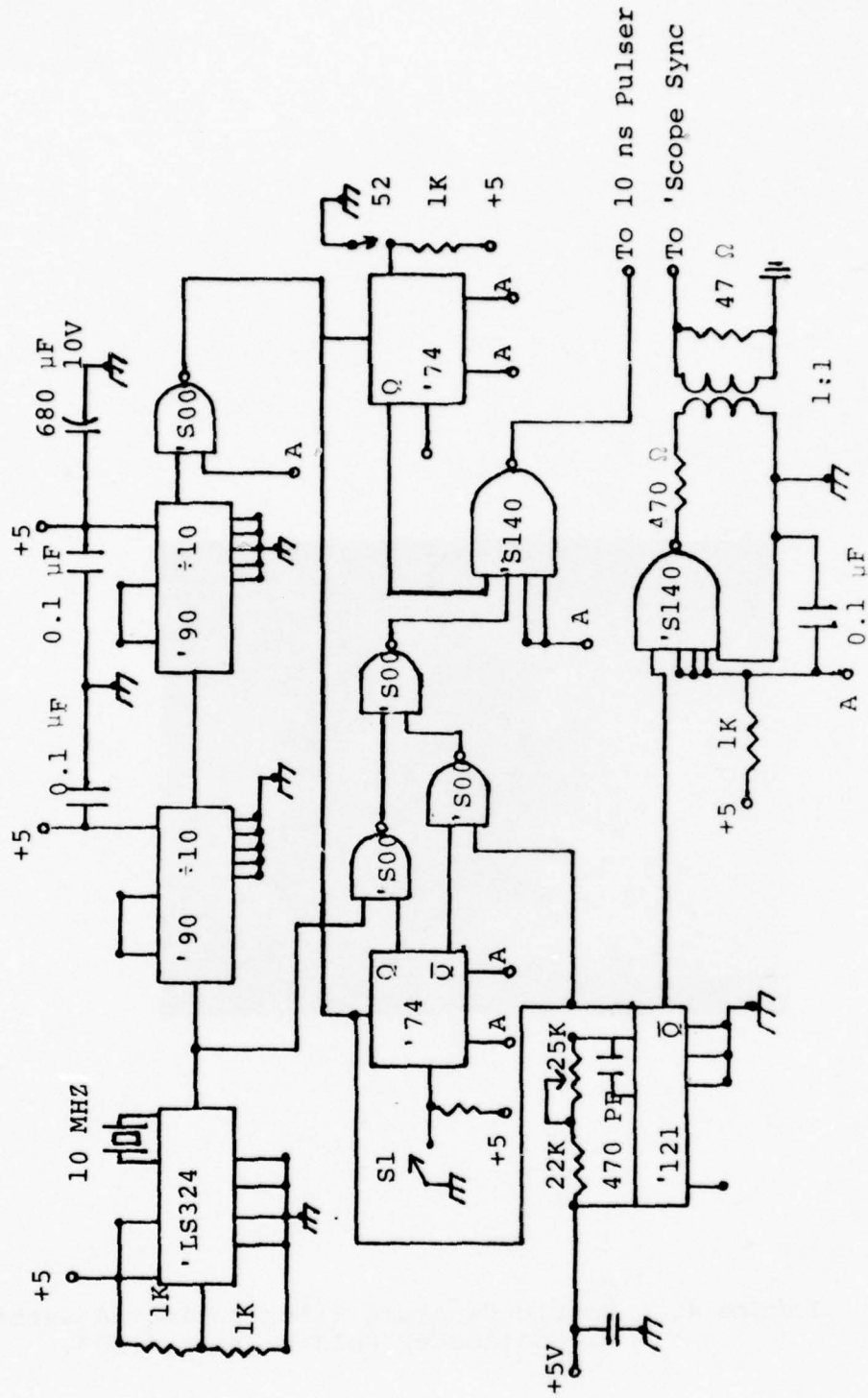


Figure 4. Double Exposure Illustrating Adjustment of Goniometer Pulser Through 90° .



Notes:

1. All Caps 0.1 F Unless Otherwise Noted.
2. All Logic 7400 Series TTL.

Figure 5. Clock and Control Circuit for Transistor Pulser.

Also, in one instance, outright oscillation has occurred. Empirical procedures in attempting to solve the problem have proven tedious and time consuming, with the frustration compounded by the good behavior of the first (substantially identical) pulser. Fortunately, the problems appear to have been solved, and construction was not substantially delayed in any event because other components in the system had not yet arrived. The clock/control circuit (Figure 5) for the transistor pulser is a TTL based design, capable of supplying 10 MHZ for 10% duty factor operation, or 100 KHZ for 0.1% duty factor operation. The master oscillator operates at 10 MHZ and is crystal controlled. Two decade down counters in succession divide the 10 MHZ by 100 to obtain the 100 KHZ. The circuit is fully buffered and uses standard switch de-bouncing techniques. A time-delayed synchronizing signal is furnished via a monostable multivibrator so that the sampling oscilloscope may be properly triggered when using the system at 100 KHZ. The time delay is variable up to about 8 μ s and is necessary because the sampling oscilloscope can display only a narrow (0.1 μ s) window when set to 5 or 10 ns per division. The proportional controller, power supplies, and fixturing for the temperature controlled pill package pulse head are all on hand. The controller is an Oven Industries Model 4C2-15 which uses an Oven Industries TP20-6 thermistor to

sense the controlled temperature. This controller requires 4 DC supplies to operate, two five volt (standard power model SPS75-5) and two twelve volt (standard power model SPS30-12). The controller, its four supplies, a three position switch to permit selection of two pre-set temperatures or variable control, and a circuit to indicate when the temperature setpoint has been reached, are all to be installed together in a suitable cabinet whose internal layout is under design. The temperature control element is a thermoelectric module (Melcor Model CP2-31-10) driven by the controller in bipolar mode to provide either heating or cooling as required to achieve the desired test temperature. The temperature control system has been tested in breadboard and works satisfactorily throughout its operating range.

The power measuring apparatus previously reported (Figures 6 and 7) continues to work well. All of the necessary components for its operation have been assembled and it is complete. The apparatus is built in two parts. One of these is the detector box (Figure 8) and it houses the TIED 88 and APD as well as its compensating regulator. The other part houses the HV power supply (Figure 9). The detector box has a rectangular aperture 1.09 cm x 0.394 cm as specified in SCS-516. Mounted behind this aperture is a piece of diffusing film (3M LDF 3008N). The APD is mounted

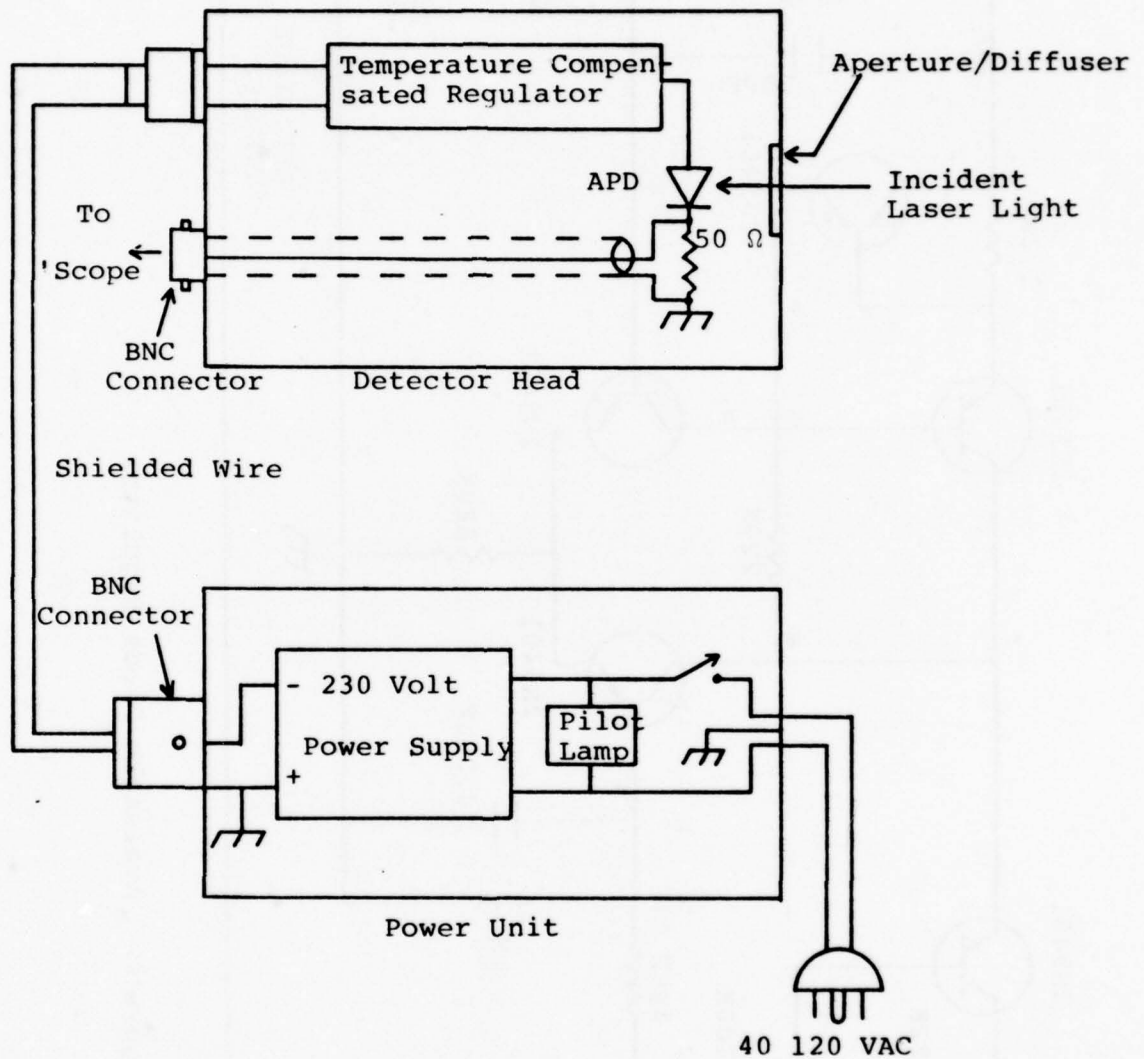


Figure 6. APD Power Detector System.

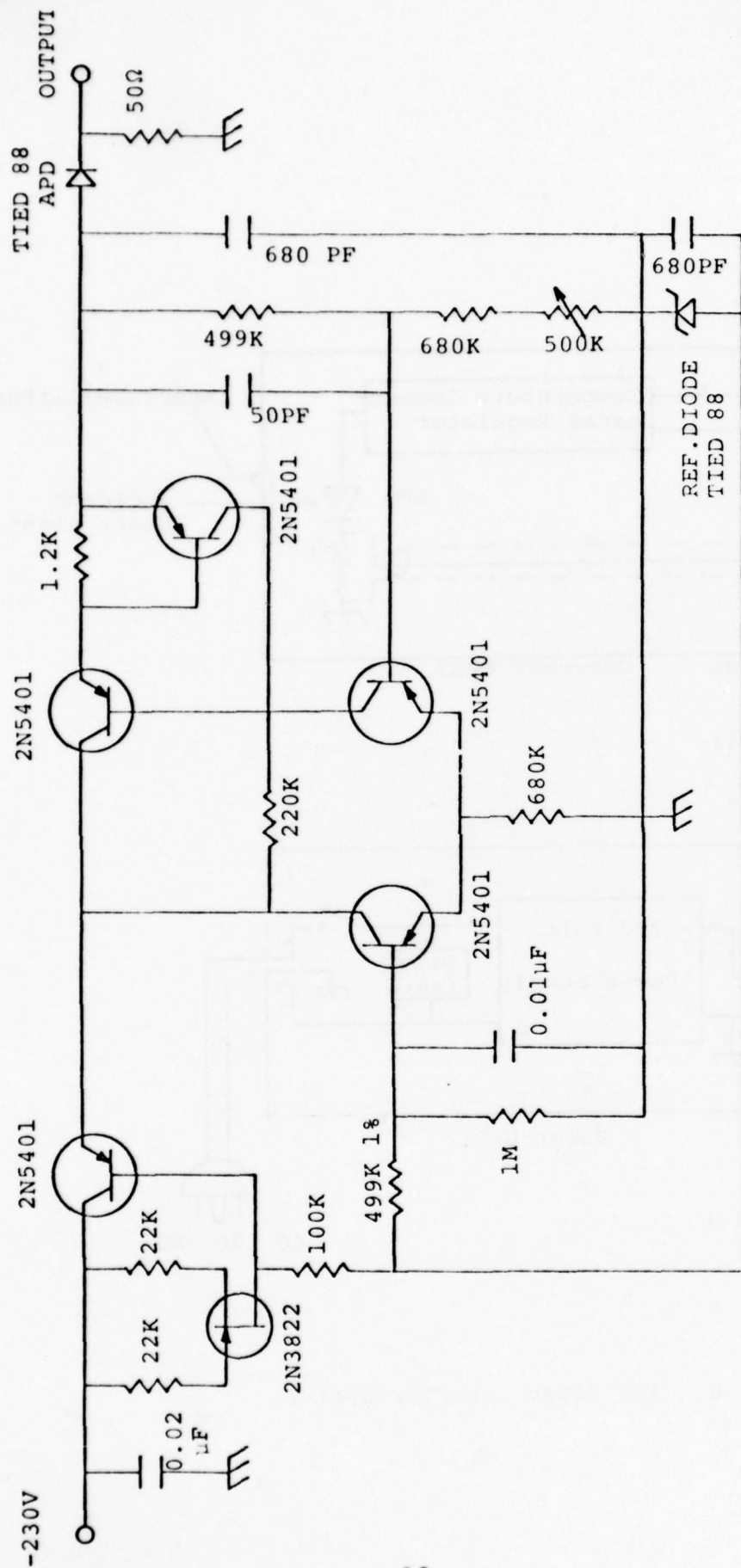


Figure 7. Avalanche Diode Regulator

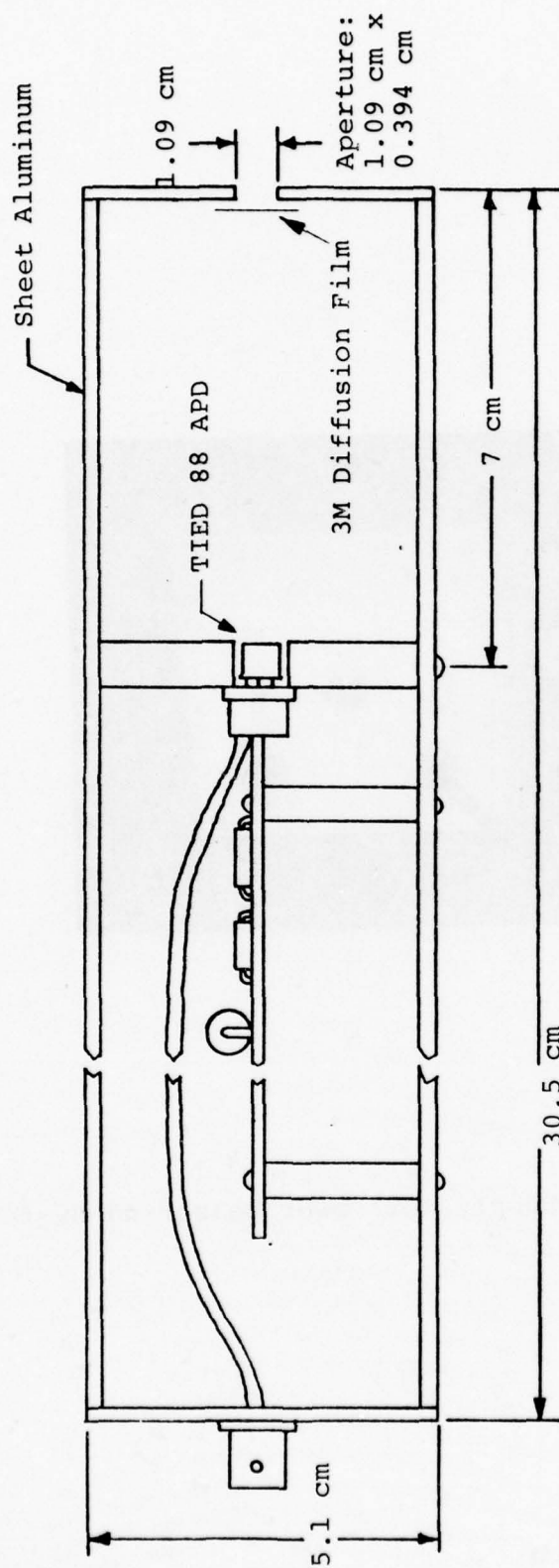


Figure 8. Internal Structure Power Detector Head.

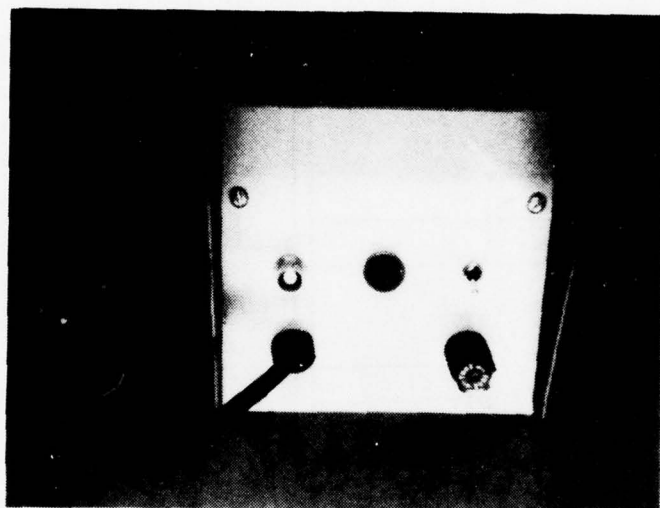


Figure 9. HV Supply for Power Measuring System.

an arbitrary distance from the diffuser. Laser light which is incident on the aperture becomes diffused into an approximately lambertian distribution such that the energy in a relatively small angle is proportional to that which is incident upon the aperture. Since the APD's active diameter is 0.762 mm and it is mounted 70 mm from the diffuser, the solid angle subtended is approximately 1.25 degrees and the approximation is much better than the overall precision of the system. The detector box is mounted to the optical rail using standard fixtures including a vertical vernier which permits accurate positioning of the detector. In use, the detector head aperture is placed 1.5 cm from the laser to be tested, the power supply is connected and turned on and the detector output is fed to one channel of the sampling oscilloscope. Since the other channel is connected to the current probe in the laser pulser and the 'scope is synchronized to the clock, the oscilloscope trace will, with appropriate calibration factors, read power output vs. drive current.

The far field ("beam pattern") apparatus (Figure 10) is also complete and ready to be used. With the 10 ns pulser (Figure 3) mounted atop the goniometer, taking care to locate the laser optical axis on the axis of rotation of the goniometer, the EG&G detector head is located between 65 cm and 75 cm away from the laser

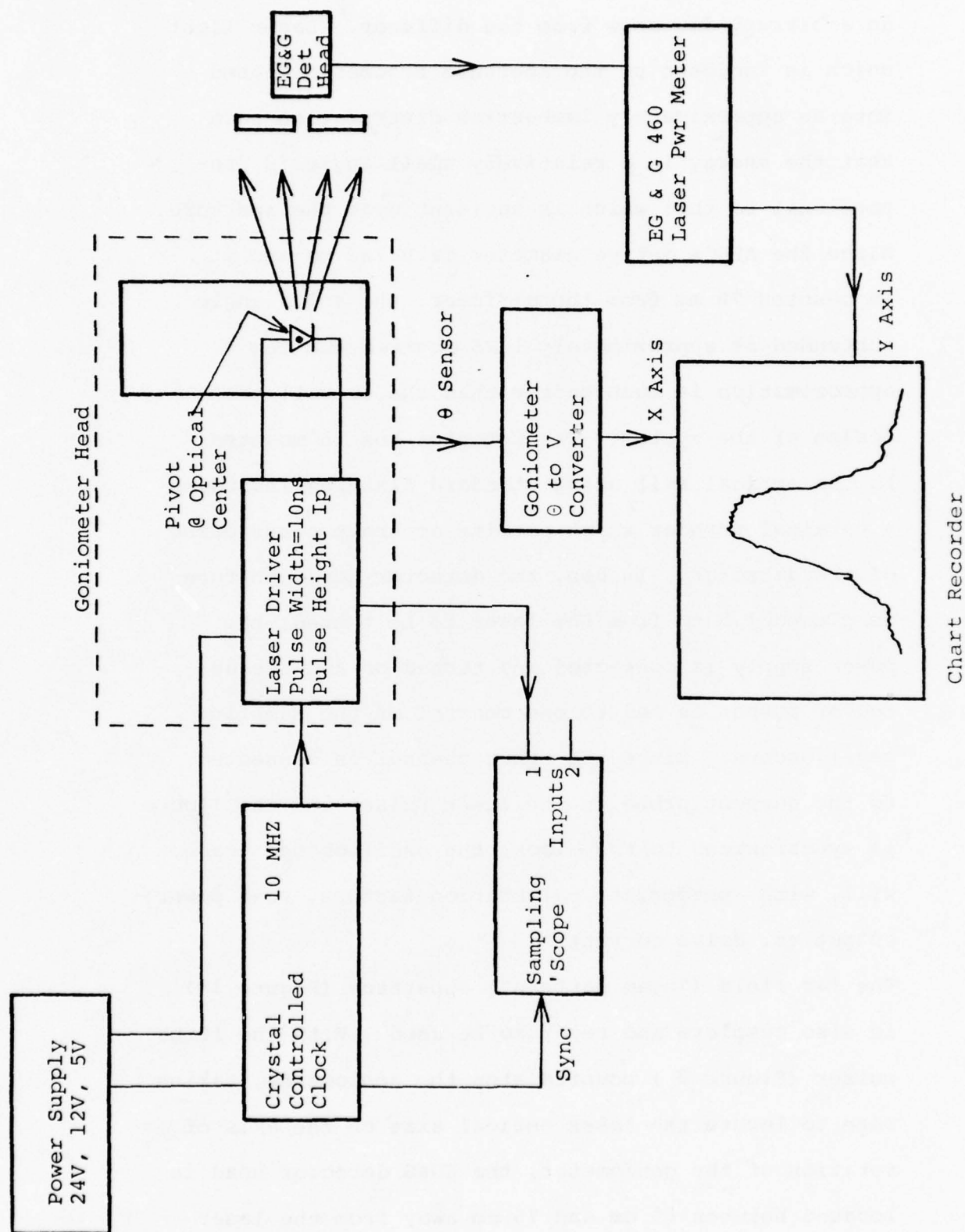


Figure 10. Measurement of Beam Width.

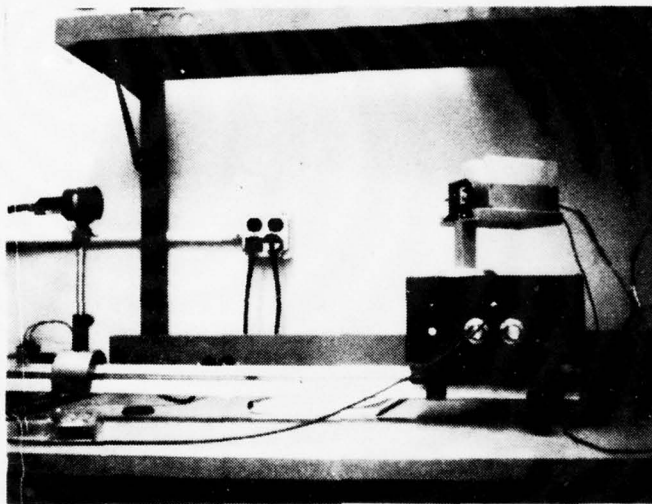


Figure 11. Photo of EG&G with Pulser on Goniometer on Rail.

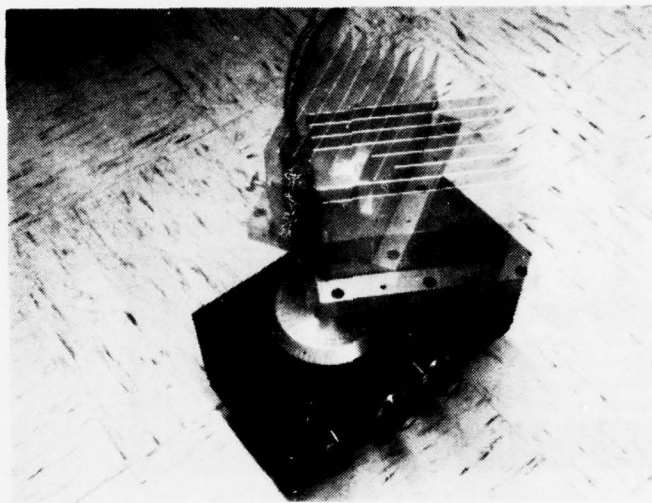


Figure 12. Double Exposure Illustrating
Goniometer Operation.

(Figure 11) so that its active area subtends a solid angle of less than one degree. The output from the goniometer drives the XY recorder's X axis and the "chart recorder output" from the EG&G 460 drives the "Y" axis. Once the appropriate scale factors are set up, the head is simply rotated manually (Figure 12) to obtain the desired graph.

Preliminary investigation indicates that a CCTV approach can probably be used to measure both stripe width and uniformity. Both a video monitor and a waveform monitor would be employed to interpret the output of the CCTV camera. The laser near field image would be projected onto a screen with a metric grid. The oscilloscope would be used to insure that saturation is avoided and, via calibration with a known grey scale, permit direct stripe to stripe relative intensity measurement. The image on the video monitor can then be measured directly by comparing the image sizes of the three stripes with the grid, thereby eliminating any errors introduced by TV scan nonlinearity.

2.3

DEVICE PERFORMANCE

Thirteen lasers have entered the second half of the 2000 hour life test without incident. It is anticipated that the second engineering sample will include units from this lot.

Early in the quarter, a preliminary 168 hour burn-in

Ft. Monmouth Laser Program - Burn-In 2nd Eng. Samples

20

TABLE 2. TYPICAL DEVICES FROM WAFER DHA-134

<u>Unit #</u>	<u>I_{th}, Amps</u>	<u>I_{max}, Amps</u>	<u>λ, nm</u>
1	1.6	2.6	807
2	1.4	2.2	810
3	1.1	2.8	813
4	1.1	2.7	812
5	1.0	2.4	810
6	1.0	2.5	815
7	1.0	2.8	810
8	1.0	2.5	805
9	1.0	3.0	809
10	1.2	2.4	812
11	1.0	2.4	815

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LASER FROM WAFER
DHA-134 @ $I_p = 2.5A$,
 $P_o = 200 \text{ mw}$

FAR FIELD PLOT
PLANE PERPENDICULAR
TO JUNCTION

20 DEGREES
PER MAJOR
DIVISION

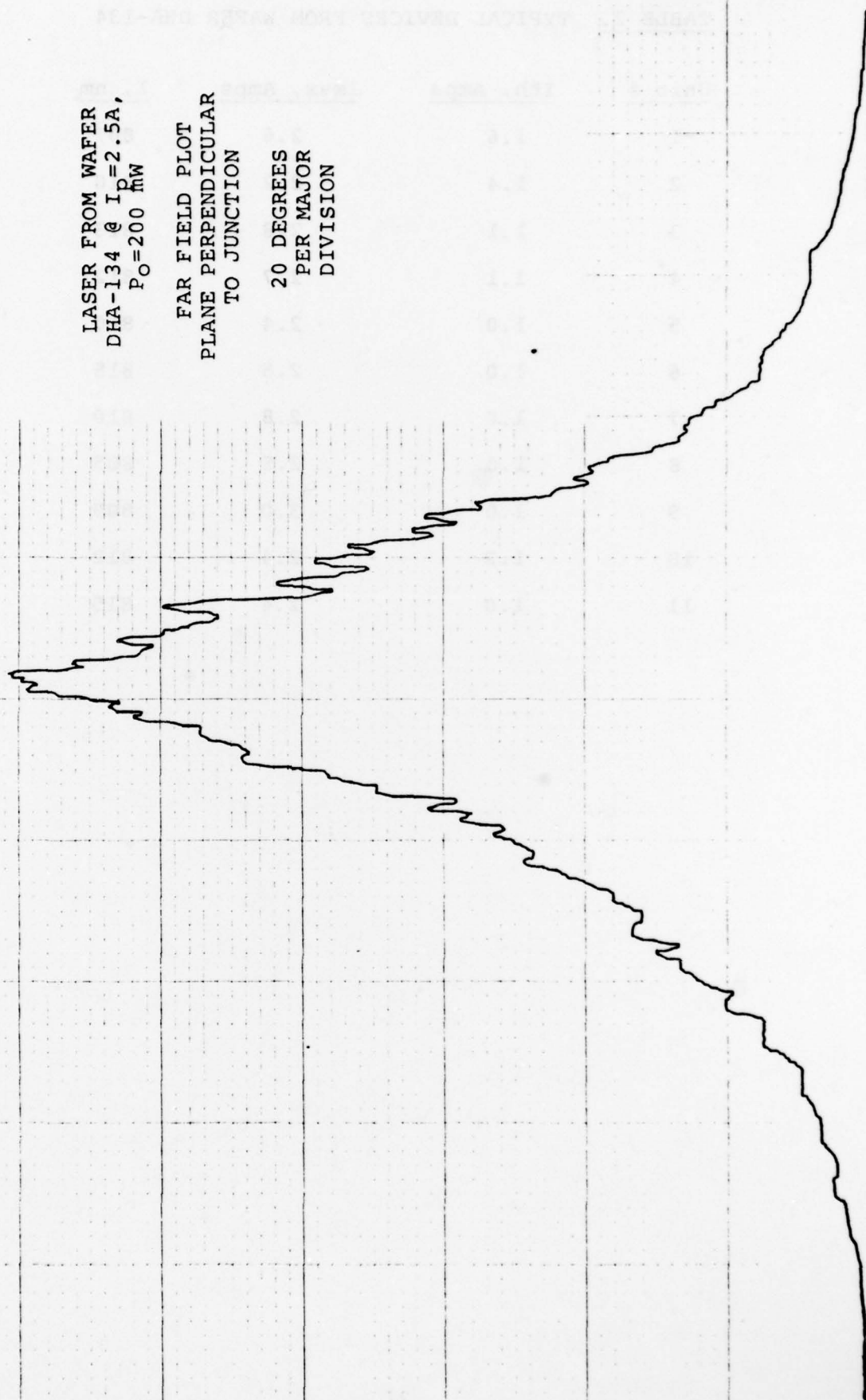


Figure 13. Laser from Wafer DHA-134.

of units was performed with emphasis directed toward selection of units for the 2000 hour life test. The 168 hour burn-in yield was about 55%, which compared favorably with the 50% yield commonly observed for other military and commercial injection laser products. From this group of devices, 16 stable lasers were selected and placed on life test to satisfy the requirements for the second engineering sample. Thirteen devices showed no significant change after 1000 hours of operation (Table 1) at 10 MHz with 10 ns pulses at I_p . Of the remaining three, two degraded below the acceptable operating range and the third was damaged by a screwdriver short to the power supply during rack adjustment.

Another wafer, DHA-134, was prepared during this period. Initial testing of devices fabricated from this wafer indicated satisfactory power output within the required drive current range. Further tests were therefore performed, with wavelength and far field test results within limits of SCS-516. These results are summarized in Table 2 and Figure 13. Many of the test setups and procedures for the test plan have been developed, while others are still in the design stages. The main work ahead principally deals with measurement of stripe width and uniformity in a manner consistent with both good accuracy and volume production.

SECTION III

SUMMARY

At the close of the fifth quarter, the second engineering sample was into the second half of the 2000 hour life test, with all indications pointing to fully successful completion and delivery in the next quarter. Principal emphasis during the quarter was directed toward completion of test methods, plans, and equipment. Although much remains to be done, the areas in need of work are clear in most cases, and a steady progress toward the goals of the next quarter is expected. These goals include delivery of the 2nd engineering sample, solution of the heating problem within the burn-in rack, completion of the temperature controlled pulser for thermal impedance measurement, and further work on the test plan.

APPENDIX A

Engineering Man-Hour Utilization for the Fifth Quarter of the Program.

	<u>5th Qtr.</u>	<u>Cumulative</u>
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R. B. Gill	15 Hrs.	799 Hrs.
S. Klunk	86 Hrs.	472 Hrs.
M. Lai	178 Hrs.	178 Hrs.
F. Speer	212 Hrs.	212 Hrs.
A. Gennaro	-	371 Hrs.
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